

UNITED STATES PATENT APPLICATION

FOR

Method and Apparatus for Coupling a Microelectronic Device Package to a
Circuit Board

INVENTOR:

Chia-Pin Chiu

Prepared by:

Blakely, Sokoloff, Taylor & Zafman LLP
12400 Wilshire Boulevard
Seventh Floor
Los Angeles, California 90025
(408) 720-8300

Attorney's Docket No. 42390P13556

"Express Mail" mailing label number EL867650659US

Date of Deposit February 12, 2002

I hereby certify that this paper or fee is being deposited with the United States Postal Service
"Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above
and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Julie Arango

(Typed or printed name of person mailing paper or fee)

Julie Arango 2/12/02

(Signature of person mailing paper or fee)

10075313-021202

Method and Apparatus for Coupling a Microelectronic Device Package to a Circuit Board

FIELD OF THE INVENTION

[0001] The present invention pertains to techniques for coupling microelectronic device packages to printed circuit boards. More particularly, the present invention relates to a technique for coupling a microelectronic device package to a printed circuit board without using a reflow or wave-solder process.

BACKGROUND OF THE INVENTION

[0002] In microelectronic device manufacturing, a microelectronic circuit chip, or "die", is commonly mounted to a "package" before it is integrated into a larger system. The package serves to protect the die and may provide a standardized interface between the die and the system in it will be used. The package with the integrated die is subsequently mounted to a printed circuit board (PCB), such as a motherboard in a computer system.

[0003] Two common types of interface for connecting these components together are the ball grid array (BGA) and the pin grid array (PGA). In the case of a BGA, the input/output (I/O) terminals of a device are formed by an array of round solder balls, while in the case of a PGA they are formed by an array of metal pins in the case of a PGA. Figures 1 and 2 show examples (in side cross-section) of how BGA components and PGA components, respectively, can be mounted directly to a PCB according to the current state of the art. In Figure 1, a

semiconductor die 1 is mounted to a device package 2, which is coupled directly to a PCB 3 (e.g., a motherboard) by solder balls 4 of a BGA. The package 2 can be mounted directly to the PCB 3 using a solder reflow process. In Figure 2, the die 1 is mounted to a package 6, which is coupled directly to a PCB 7 (e.g., a motherboard) by pins 8 of a PGA. The package 6 can be mounted directly to the PCB 7 by inserting the pins 8 into through holes 9 on the PCB 7 and then using a wave-solder process.

[0004] One problem with directly mounting a BGA or PGA directly to a PCB is that it is difficult if not impossible to interchange or rework components once they are mounted to the PCB. Consequently, sockets are often used to mount a BGA or PGA to a PCB, to provide greater parts interchangeability and reworkability.

[0005] With each device generation, particularly for microprocessors, the number of inputs and outputs required in the device tends to increase. As the pin/ball count increases, the cost of a traditional PGA/BGA socket tends to increase. In addition, the traditional socket requires a surface mount or wave-soldering process to mount it on the motherboard, which are expensive and complicated processes.

[0006] Moreover, in view of the steadily increasing demand for smaller, more mobile computing and communication devices, there is an increasing emphasis on conserving the amount of space consumed by electronic components within a system. Although BGA/PGA sockets provide interchangeability and

reworkability, they also add to the total height of electronic components on a PCB, thereby working against space conservation efforts.

[0007] Consequently, it is desirable to have an alternative technique for mounting microelectronic components to a PCB, which overcomes these disadvantages.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] The present invention is illustrated by way of example and not limitation in the figures of the accompanying drawings, in which like references indicate similar elements and in which:

[0009] Figure 1 illustrates how a BGA can be mounted to a PCB;

[0010] Figure 2 illustrates how a PGA can be mounted to a PCB;

[0011] Figure 3 illustrates two orthogonal views of the PCB with multiple through holes, each filled with a conductive elastomer;

[0012] Figure 4A shows how a BGA can be mounted to the PCB of Figure 3;

[0013] Figure 4B shows how a PGA can be mounted to the PCB of Figure 3;

[0014] Figures 5A through 5D show various techniques for fastening a BGA-based microelectronic component to a PCB; and

[0015] Figures 6A through 6D are cross-sectional side views of the PCB showing how the elastomer can be electrically coupled to the electrical traces of the PCB.

DETAILED DESCRIPTION

[0016] A method and apparatus for coupling a microelectronic device package to a printed circuit board without using a reflow or wave-solder process are described. Note that in this description, references to "one embodiment" or "an embodiment" mean that the feature being referred to is included in at least one embodiment of the present invention. Further, separate references to "one embodiment" in this description do not necessarily refer to the same embodiment; however, neither are such embodiments mutually exclusive, unless so stated and except as will be readily apparent to those skilled in the art. For example, a feature, structure, act, etc. described in one embodiment may also be included in other embodiments. Thus, the present invention can include a variety of combinations and/or integrations of the embodiments described herein.

[0017] Refer now to Figure 3, which illustrates a top view and an orthogonal side cross-section view of a PCB 31 in accordance with the present invention. Multiple through holes 32 are drilled through the PCB (e.g., motherboard) 31 substrate, and each through hole 32 is then filled with a malleable, electrically conductive material 33. In one embodiment the material 33 is a conductive elastomer (as henceforth assumed herein to facilitate description), e.g., an elastomer containing a certain concentration of conductive particles. The elastomer 33 is made to be flush with the top surface and bottom surface of the PCB 31.

[0018] The elastomer 33 in each through hole 32 forms an electrical contact at which a solder ball or pin of a BGA or PGA of a microelectronic package, respectively, will be coupled to the PCB 31. As shown in the side cross-section view, the through holes 32 are tapered toward the bottom side of the PCB 31 to prevent the elastomer 33 from being pushed out the bottom side of the PCB 31 when the package is attached to the PCB 31.

[0019] The elastomer 33 can be connected directly, or through conductive plating on the wall of the through holes, to surface traces or internal conductive planes inside the motherboard, as shown in Figures 6A through 6D. Figure 6A shows the elastomer 33 directly in contact with the internal planes 66 of the PCB 31. Figure 6B shows the elastomer 33 in contact with the internal planes 66 of the PCB 31 through the conductive (e.g., copper) plating 67 on the wall of the through hole. Figure 6C shows the elastomer 33 directly in contact with the surface planes 68 of the PCB 31. Figure 6D shows the elastomer 33 in contact with the surface planes 68 of the PCB 31 through the conductive (e.g., copper) plating 67 on the wall of the through hole. Note that these embodiments can be combined in a given PCB.

[0020] Figures 4A and 4B show examples of the application of the above described technique. Specifically, Figure 4A shows how a BGA-based package 43 can be mounted to the PCB 31, while Figure 4B shows how a PGA-based package 44 can be mounted to the PCB 31. In Figure 4A, the BGA is directly mounted on the PCB 31 by applying clamping force between the solder balls 41

and the elastomer 33, causing a slight compression of the elastomer 33. As described further below, the clamping force could come from the thermal enabling solution such as heat sink clip, heat sink joint bolt, or heat sink spring. The clamping force can also come from an independent clamping mechanism regardless of the thermal solutions.

[0021] In Figure 4B, the PGA is directly mounted on the PCB 31 by inserting the package pins 42 through the elastomer 33. The package 43 can then be held on the motherboard by either a heat sink clamping mechanism or an independent clamping mechanism.

[0022] The elastomer 33 generally may be of the type commonly used in electronics applications, such as used to make gaskets in electromagnetic interference (EMI) applications. A typical elastomer used in such an application is soft enough for a package pin to penetrate through it. A typical elastomer thickness can be compressed approximately 15 percent under certain pressure so that the elastomer can conform to an irregular surface shape, such as a solder ball. However, most such elastomers do not have good electrical conductivity. To make the elastomer electrically conductive, therefore, particles of silver (Ag), copper (Cu), nickel (Ni), or gold (Au), or other suitable conductive element(s), can be added into the elastomer while it is in liquid form, prior to filling it in the through holes 32 of the PCB 31. A concentration of conductive particles of at least approximately 50 percent is believed to be sufficient to provide adequate conductivity.

10075513-021202

[0023] Figures 5A through 5D show various techniques for fastening a BGA- or PGA-based microelectronic component to the PCB 31 (only BGA-based embodiments are shown, to simplify the description). Figure 5A shows an embodiment in which a single clip 51 is used to provide the clamping force. The clip 51 applies pressure against the top surface of the die 45 and the bottom surface of the PCB 31. The clip 51 passes through two additional through holes 55 in the PCB 31.

[0024] Figure 5B shows an embodiment in which a clamp-and-bolt assembly 58 is used to provide the clamping force between the package 43 and the PCB 31. In multiple locations, the combination of a bolt 59 and nut 57 apply a force against a clamp 60 to compress the package 43 against the PCB 31.

[0025] Figure 5 C shows an embodiment to similar to Figure 5A, except that multiple clips 62 are used to provide the clamping force, which apply pressure directly against the package 43 rather than against the die 45.

[0026] Figure 5D shows a spring-based embodiment. In multiple locations, a compressed spring 63 applies a force against the bottom of the PCB 31 and against a nut 64 to provide the clamping force.

[0027] From the foregoing description, the following advantages of this technique will be apparent. The contacts for coupling the pins or solder balls to the PCB are built into the PCB; no separate socket is required. Furthermore, the malleable, compressible nature of the elastomer allows a device package to be removed from the PCB and a new package to be mounted without damage,

thereby providing part interchangeability and reworkability. The technique can be used for both PGA and BGA types of packages and can be extended for higher pin/ball count and smaller pin/ball pitch. In addition, the compressibility of the elastomer can compensate for different ball height (e.g., due to design or assembly variation) or warping of the PCB substrate. The technique can also compensate for the problem of pins' actual locations varying from their ideal (design) locations. The technique may also avoid current (I_{max}) constraints caused by the long electrical connection paths from the PCB to the package when sockets are used.

[0028] Thus, a method and apparatus for coupling a microelectronic device package to a printed circuit board without using a reflow or wave-solder process have been described. Although the present invention has been described with reference to specific exemplary embodiments, it will be evident that various modifications and changes may be made to these embodiments without departing from the broader spirit and scope of the invention as set forth in the claims. Accordingly, the specification and drawings are to be regarded in an illustrative sense rather than a restrictive sense.